LOGIC EMBEDDED-MEMORY INTEGRATED CIRCUITS

ABSTRACT

[0027] A semiconductor device and a method for fabricating such a device is disclosed. In one example, the semiconductor device includes a substrate, one or more logic devices formed over the substrate, and one or more memory devices formed over the substrate. The logic device may include a high dielectric constant (high-k) gate dielectric, while the memory device may include a non-high-k gate dielectric.